



**Dalicap**

DALICAP TECH. CORPORATION

**DLC70P High Q. RF/Microwave Multilayer Chip Ceramic Capacitors**

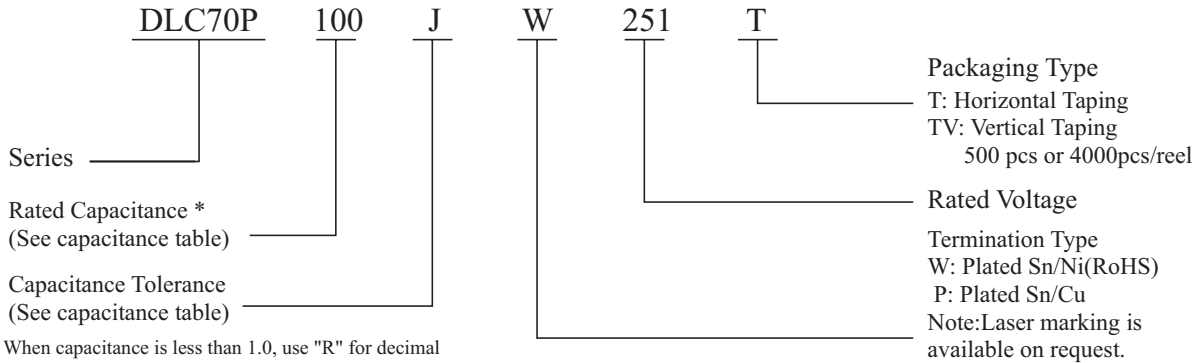
**DLC70P(.060" x.030")**

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**◆ DLC70P Capacitance & Rated Voltage Table**

Cap.pF	Code	Tol.	Rated WVDC	Cap.pF	Code	Tol.	Rated WVDC	Cap.pF	Code	Tol.	Rated WVDC
0.1	0R1	A,B, C,D	250V Code 251	2.2	2R2	A,B, C,D	250V Code 251	16	160	F,G, J	250V Code 251
0.2	0R2			2.4	2R4			18	180		
0.3	0R3			2.7	2R7			20	200		
0.4	0R4			3.0	3R0			22	220		
0.5	0R5			3.3	3R3			24	240		
0.6	0R6			3.6	3R6			27	270		
0.7	0R7			3.9	3R9			30	300		
0.8	0R8			4.3	4R3			33	330		
0.9	0R9			4.7	4R7			36	360		
1.0	1R0			5.1	5R1	39		390			
1.1	1R1			5.6	5R6	43		430			
1.2	1R2			6.2	6R2	47		470			
1.3	1R3			6.8	6R8	51		510			
1.4	1R4			7.5	7R5	56		560			
1.5	1R5			8.2	8R2	62		620			
1.6	1R6			9.1	9R1	68		680			
1.7	1R7			10	100						
1.8	1R8			11	110						
1.9	1R9			12	120						
2.0	2R0			13	130						
2.1	2R1			15	150						

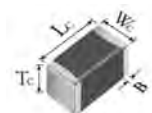

◆ **Part Numbering**



Code	A	B	C	D	F	G	J
Tolerance	± 0.05pF	± 0.1pF	± 0.25pF	± 0.5pF	± 1%	± 2%	± 5%

◆ **DLC70P Capacitor Dimensions**

unit:inch(millimeter)

Series	Term. Code	Type / Outlines	Capacitor Dimensions			Plated Material
			Length (Lc)	Width (Wc)	Thickness (Tc)	
DLC70P	W		.060 ± .006 (1.52 ± 0.15)	.030 ± .006 (0.81 ± 0.15)	.030+.005 ~ -.003 (0.76+0.13 ~ -.08)	Sn/Ni (RoHS)
	L	Chip				90 Sn10Pb/Ni
DLC70P	P (Non-Mag)	 Chip (Non-Mag)				Sn/Cu (RoHS)

◆ **Design Kits**

These capacitors are 100% RoHS. Kits are available in Magnetic and Non-Magnetic that contain 10(ten) pieces per value; number of values per kit varies, depending on case size and capacitance.

Kit	Description (pF)	Values (pF)	Tolerance
DKDLC70P01	0.1 - 2.0	0.1, 0.2, 0.3, 0.4, 0.5, 0.6, 0.7, 0.8, 0.9, 1.0, 1.2, 1.5, 1.6, 1.8, 2.0	± 0.10pF
DKDLC70P02	1.0 - 10	1.0, 1.2, 1.5, 1.8, 2.0, 2.2, 2.4, 2.7, 3.0, 3.3, 3.9, 4.7, 5.6, 6.8, 8.2	± 0.10pF
		10	± 5%
DKDLC70P03	10 - 68	10, 12, 15, 18, 20, 22, 24, 27, 30, 33, 39, 47, 56, 68	± 5%



**DLC70P High Q. RF/Microwave Multilayer Chip Ceramic Capacitors**

**DLC70P(.060" x.030")**

◆ **Performance**

Item	Specifications
Quality Factor (Q)	2,000 min.
Insulation Resistance (IR)	10 <sup>5</sup> Megohms min. @ +25°C at rated WVDC. 10 <sup>4</sup> Megohms min. @ +125°C at rated WVDC.
Rated Voltage	250V
Dielectric Withstanding Voltage (DWV)	250% of rated voltage for 5 seconds.
Operating Temperature Range	-55°C to +200°C
Temperature Coefficient (TC)	0 ± 30 ppm/°C (-55°C to +175°C)
Capacitance Drift	± 0.02% or ± 0.02pF, whichever is greater.
Piezoelectric Effects	None

◆ **Environmental Tests**

Item	Specifications	Method
Thermal Shock	DWV: the initial value IR: Shall not be less than 30% of the initial value Capacitance change: no more than 0.5% or 0.5pF, whichever is greater.	MIL-STD-202, Method 107, Condition A. At the maximum rated temperature(-55°C and 200°C) stay 30 minutes. The time of removing shall not be more than 3 minutes. Perform the five cycles.
Moisture Resistance		MIL-STD-202, Method 106.
Humidity (steady state)	DWV: the initial value IR: the initial value Capacitance change: no more than 0.3% or 0.3pF, whichever is greater.	MIL-STD-202, Method 103, Condition A, with 1.5 Volts D.C. applied while subjected to an environment of 85°C with 85% relative humidity for 240 hours minimum.
Life	IR: Shall not be less than 30% of the initial value Capacitance change: no more than 2.0% or 0.5pF, whichever is greater.	MIL-STD-202, Method 108, for 2000 hours, at 200°C. 200% Rated voltage D.C. applies

◆ **DLC70P Electrical Performance**

ESR vs Frequency

